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2.21. 2.22. Unlike Clr, the output from the mux is only read on falling clock edges; therefore, adding C to the

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sensitivity list is not required for proper operation of the circuit. 2.23 (a) sel ...

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Results of applying PSI to a first course in logic design of digital systems are described in Roth, C.H., "Continuing Effectiveness of Personalized Self-Paced Instruction in Digital Systems Engineering", Engineering Education, Vol. 63, No. 6, March 1973.

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2.1 (a) VHDL - VHSIC Hardware Description Language VHSIC - Very High Speed Integrated Circuit (b) VHDL has statements that execute concurrently since it must model real hardware in which the components are all in operation at the same time. (c) A hardware description language allows a digital system to be designed and

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(c) A hardware description language allows a digital system to be designed and debugged at a higher level of abstraction than schematic capture with gates, flip-flops, and standard MSI building blocks. The details of the gates

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and flip-flops do not need to be handled during early phases of design.

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Charles Roth is Professor Emeritus in Electrical and Computer Engineering at the University of Texas at Austin, where he taught Digital Design for more than

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four decades. He is the author of Fundamentals of Logic Design, which is in its sixth edition, and Digital Systems Design using VHDL, which is in its second edition.

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